## DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am an original, first and joint inventor (if plural names are listed below) of the subject matter: which is claimed and for which a patent is sought on the invention entitled:

## METHOD OF PLANARIZING A SEMICONDUCTOR DIE

the specification of which Application No	ch (check one) X is attached in a same in and was amen	ched hereto or was filed on	as (if applicable).
	e reviewed and understand amended by any amendme	the contents of the above-iden nt referred to above.	tified specification,
I acknowledge the duty 1.56.	to disclose all information	which is material to patentabil	lity as defined in 37 CFR §
application(s) for patent designated at least one of	or inventor's certificate, or country other than the Unite for patent or inventor's cert	S.C. § 119(a)-(d) or § 365(b) § 365(a) of any PCT Internated Ed States, listed below and have tificate having a filing date before	ional application which re also identified below
Prior Foreign Application(s)			Priority Claimed Yes No
Number	Country	Day/Month/Year Filed	<del></del>
Number	Country	Day/Month/Year Filed	·
I hereby claim the bene	fit under 35 U.S.C. § 119(e	e) of any United States provision	onal application(s) below.
60/422,314	October 29, 2002		
Application Number	Filing Date		
Application Number	Filing Date	· · · · · · · · · · · · · · · · · · ·	

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Filing Date	Status: Patented, Pending, Abandoned	
Application Number	Filing Date	Status: Patented, Pending, Abandoned	

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

ruii name oi iirsi invent	or <u>AMITAY LEVI</u>		
Inventor's signature	Lem Senty	4/16/03 Date	
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Attorney Docket No.: 2102397-992431

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: LEVI, ET AL.

Group Art Unit: Not Yet Assigned

Serial No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed:

April 25, 2003

Title:

METHOD OF PLANARIZING A SEMICONDUCTOR DIE

**POWER OF ATTORNEY BY ASSIGNEE** 

Assistant Commissioner for Patents Washington, DC 20231

Sir:

The undersigned, being Assignee of the entire right, title and interest in the above-identified application by virtue of an Assignment, a copy thereof is attached, under 37 C.F.R. §3.71, to prosecute the application to the exclusion of the Inventor(s), and grants Power of Attorney and correspondence address as stated herein.

Silicon Storage Technology, Inc., hereby appoints:

Practitioners at Customer Number: 26379

26379 PATENT TRADEMARK OFFICE

Attorney Docket No.: 2102397-992431

as attorneys/agents with full power of substitution and revocation to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Assignee:

Silicon Storage Technology, Inc.

Date: April 14, 2003

Printed Name: Bing Yeh

Title: President and CEO